


Form PTO-1449		Atty Docket No.: 042390.P8653	Serial No.: 09/607,580
INFORMATION DISCLOSURE CITATION List of Patents and Publications Statement (Use several sheets if necessary) Sheet 1 of 1		Applicant: Intel Corporation Filing Date: June 29, 2000	

U.S. PATENT DOCUMENTS

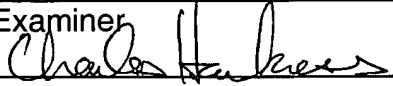
Examiner Initials	Document No.	Patentee	Class	Sub-Class	Filing date if appropriate
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FOREIGN PATENT DOCUMENTS

No.	Document No.	Date	Country	Class	Sub-Class	Translation
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

CH	AA	Connors, D., et al., "Compiler-Directed Dynamic Computation Reuse: Rationale and Initial Results," 32th International Symposium on Microarchitecture (Micro32), Nov.-Dec. 1999.
CH	AB	Marcuello, P., et al., "Value Prediction for Speculative Multithreaded Architectures," 32th International Symposium on Microarchitecture (Micro32), Nov.-Dec. 1999.
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Examiner 	Date Considered 8/19/03
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	